



EW
Cofc

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert H. Dennard, et al.

Docket: 15667A

Patent No.: 6,815,296

Dated: April 6, 2005

Issued: November 9, 2004

For: POLYSILICON BACK-GATED SOI MOSFET
FOR DYNAMIC THRESHOLD VOLTAGE
CONTROL

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION

Sir:

It appears that errors have been introduced in the course of printing the Patent issued in the above application, and it is respectfully requested that the Commissioner issue a Certificate of Correction in the following respects:

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

Dated: April 6, 2005

Leslie S. Szivos

Column 1, Line 9 "related to U.S." should read -- related to co-assigned U.S. --

Respectfully submitted,



Leslie S. Szivos
Reg. No. 39,394

Scully, Scott, Murphy & Presser
400 Garden City Plaza, Suite 300
Garden City, New York 11530
(516) 742-4343
/al

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO : **6,815,296**
DATED : **November 9, 2004**
INVENTOR(S) : **Robert H. Dennard, et al.**

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, Line 9 "related to U.S." should read -- related to co-assigned U.S. --

MAILING ADDRESS OF SENDER:

Scully, Scott, Murphy & Presser
400 Garden City Plaza, Suite 300
Garden City, New York 11530

PATENT NO. **6,815,296**

No. of additional copies

1